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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:	Chang Choo, et al.	Confirmation No.:	1045
Serial No.:	09/863,650	Art Unit:	2124
Filed:	May 22, 2001	Examiner:	Chuong D. Ngo
For:	APPARATUS AND METHOD FOR IMPLEMENTING EFFICIENT ARITHMETIC CIRCUITS IN PROGRAMMABLE LOGIC DEVICES	Attorney Docket No.:	060889-0038

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The enclosed Amendment is in response to the Office Action dated September 14, 2004 for the above identified patent application.